How to verify the proper Heat Sink

In certain instances, once the heat sink requirements for a SSR in a particular application have been determined and installed, (see the Crydom paper entitled SELECTING A SUITABLE HEAT SINK), it may be desirable to verify that the system does indeed provide adequate cooling to ensure reliable SSR operation.

The following is a relatively simple method to check this suitability, and essentially uses some of the calculations from SELECTING A SUITABLE HEAT SINK in a reverse manner. This method may also be used on existing systems in the field that might have been more or less “empirically” designed, to gain information on their performance and potential reliability. This method involves determining the temperature of the internal power devices, (SCR’s or Triac), within the SSR and then comparing that temperature with a “standard” absolute maximum temperature that the SSR power devices must never exceed. The maximum power device temperature is generally considered to be 125°C but for an added safety margin, 115°C should be used. (Of course for the truest indication, the entire system being evaluated should be stable and operating at its maximum rated parameters including load currents, ambient temperatures, and with doors and access panels in their normal operating positions.)

There are three additional pieces of data needed to perform this evaluation. They are the actual load current switched by the SSR, the specified “Thermal Resistance – Junction to Case” - $R_{θjc}$ of the SSR, and the measured temperature of the SSR base plate. Ideally, the temperature measurement of the SSR base plate should be taken directly from the bottom center of the SSR. However, since in most installations this is not practical since the SSR is mounted to a heat sink surface, the next best accessible location is on the top surface of the base plate near the mounting screw holes at the junction of the plastic case to the base plate surface. (To compensate for this measurement location, it is a good practice to add 3 to 5 degrees to the actual measurement.)

Using the above data, and an estimated power drop of 1 Watt for every 1 Arms of load current, the total internal dissipation in Watts can be calculated. (e.g. 35 Arms load = 35 Watts of internal dissipation.) Next, multiply the internal dissipation in Watts by the $R_{θjc}$ value, (in °C/W), to determine the internal temperature rise. This temperature value is added to the measured base plate temperature to arrive at the calculated temperature of the internal power devices. If this value is less than the 115°C “standard maximum with safety margin” value, then the heat sink is adequate.

In some cases, the heat sink information and derate curves provided within the SSR specification sheets may include expected base plate temperatures, but generally do not consider the safety margin values.
Selecting a Suitable Heatsink

Due to the forward voltage drop of the output SCRs, solid state relays generate an internal power loss. The amount of power generated is a function of the load current. The manufacturer provides power loss curves, as shown in Fig 1. At normal load currents the power loss can be estimated at 1 Watt for every 1 Arms of load current.

In order to maintain an acceptable power switch junction temperature, some form of heatsink must dissipate the heat generated by the power loss. For most printed circuit board types, the relay current rating is established by measuring the thermal impedance, from the dissipating elements to air, using the relay package as the heat sink. Some printed circuit board types are available with an integral heatsink; their ratings reflect the additional effects of the integral heatsink.

Panel mount relays usually require an external heatsink. The electrical analogy shown below identifies the primary thermal impedances in the path from junction to ambient air:

\[
T_j = \text{Power} \times (\text{sum of thermal impedances})
\]

Obviously the junction temperature \( T_j \) can be calculated if the power dissipation is known. The normal maximum allowable junction temperature is 125 degrees Centigrade. Most designs are based on providing a 10 degree Centigrade safety margin and use a heat sink to keep the junction temperature to 115 degrees Centigrade.

In other documents:
- Why Use Solid State Relays?
- SSRs - The Inside Story
- Selecting a Solid State Relay
- DC Output Solid State Relays
- SSR Overvoltage Protection
general a thermal compound will give the lowest thermal impedance, but it is very important to use a minimum of thermal compound – too much is almost as bad as none at all. One widely used technique is to apply a thin layer of compound and then apply pressure to the relay while rotating it back and forth to squeeze any excess compound out before attaching the relay to the mounting surface.

In some applications the relay will be mounted directly to a panel. This technique will work up to load currents of 7 to 8 Arms depending on the panel material, ambient temperature, etc. In these cases it is absolutely essential to mount the relay to an unpainted surface and use a good thermal compound. As a rule, the minimum per-relay panel area should be 25 square inches.

Above these current levels some form of heatsink will be required. Determine heatsink thermal impedance using rating curves like those shown in Fig 1.

As shown by the thermal impedance curves for HS-1 and HS-2 heat sinks in Fig 2, not only is the surface area of the heatsink a factor but also the power being dissipated. There are many sources for heat sinks, enabling designers to select one most suitable thermally and mechanically for the application.